

1

2

3

4

A

A

B

B

C

C

D

D

U\_LOGIC\_FPGA  
LOGIC\_FPGA.SchDoc



U\_DIGITAL\_IO  
DIGITAL\_IO.SchDoc



SCRW1  
SCRW - 4-40x1/4-Nylon  
SCRW2  
SCRW - 4-40x1/4-Nylon



SCRW3  
SCRW - 4-40x1/4-Nylon  
SCRW4  
SCRW - 4-40x1/4-Nylon



STDOFF1  
STDOFF - 4-40-5/8"  
STDOFF2  
STDOFF - 4-40-5/8"



M1  
Rubber Bumper  
Rubber Bumper Standoff



U\_PERIPHERAL\_PORT\_IO  
PERIPHERAL\_PORT\_IO.SchDoc



U\_RAM  
RAM.SchDoc



U\_POWER\_SUPPLY  
POWER\_SUPPLY.SchDoc



U\_FPGA\_POWER\_AND\_CONFIGURATION  
FPGA\_POWER\_AND\_CONFIGURATION.SchDoc



|           |                   |               |                                                                                       |
|-----------|-------------------|---------------|---------------------------------------------------------------------------------------|
| Title     | LOGi-Pi Top Level |               |  |
| Revision: | R1.5              | Sheet         |                                                                                       |
| Date:     | 11/24/2015        | Engineer:     |                                                                                       |
|           |                   | Michael Jones |                                                                                       |

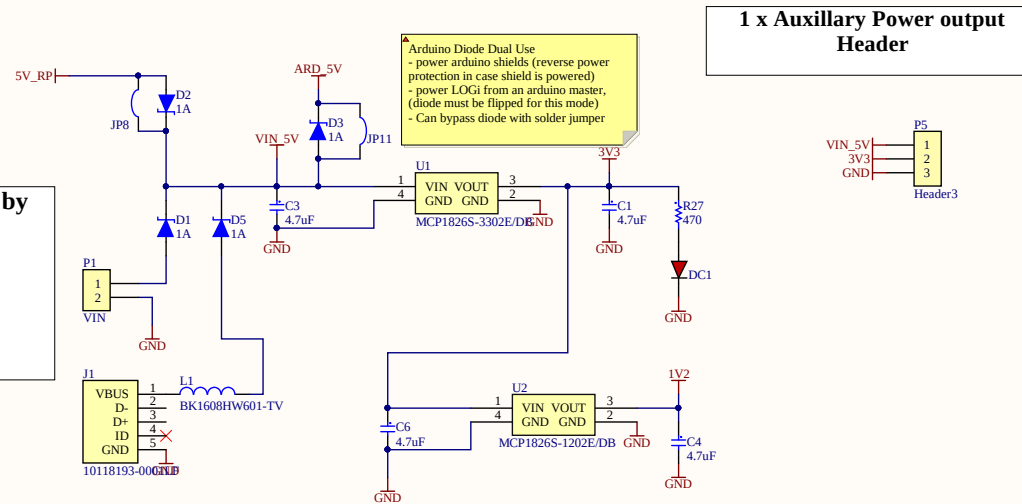
1

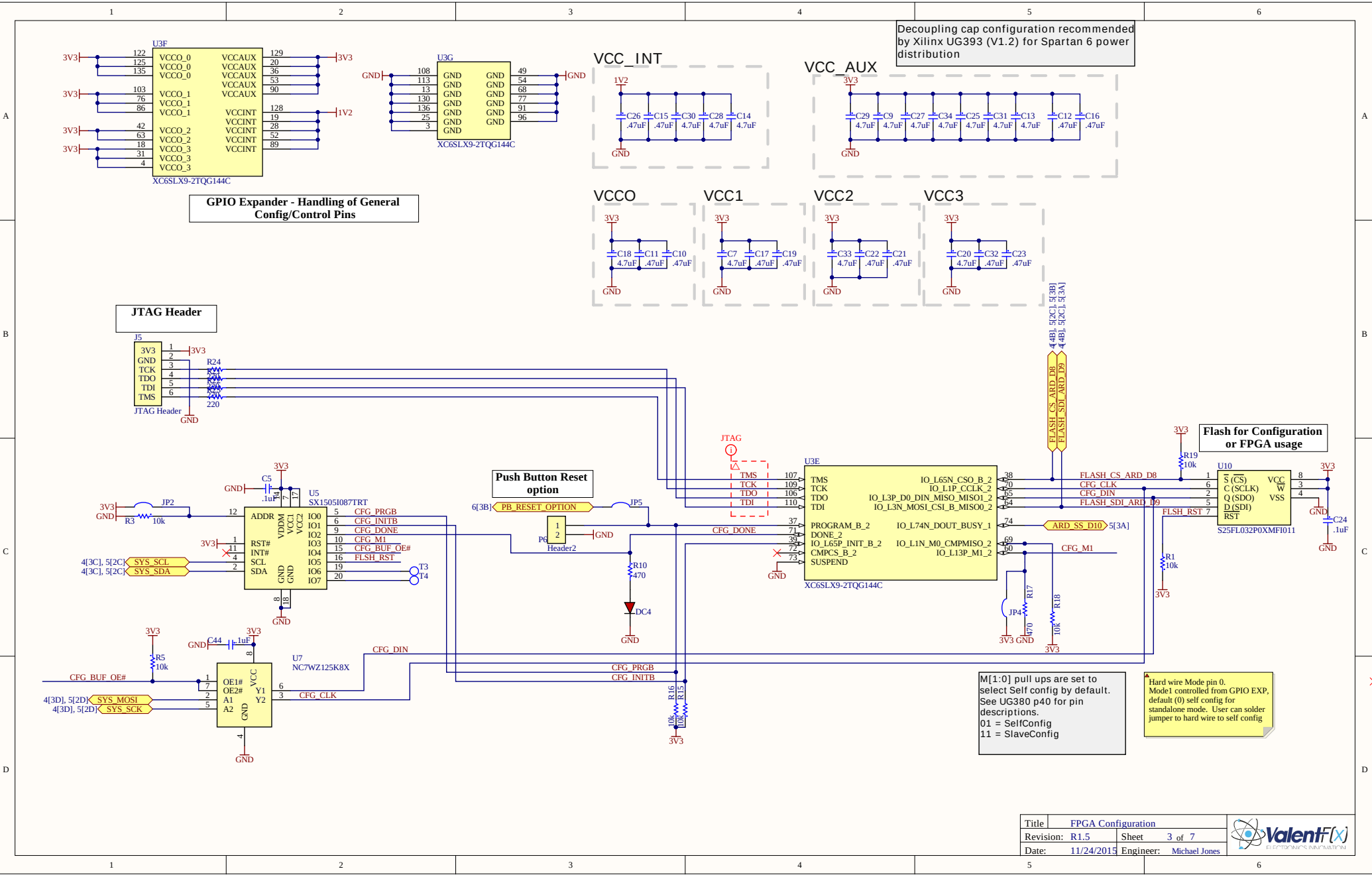
2

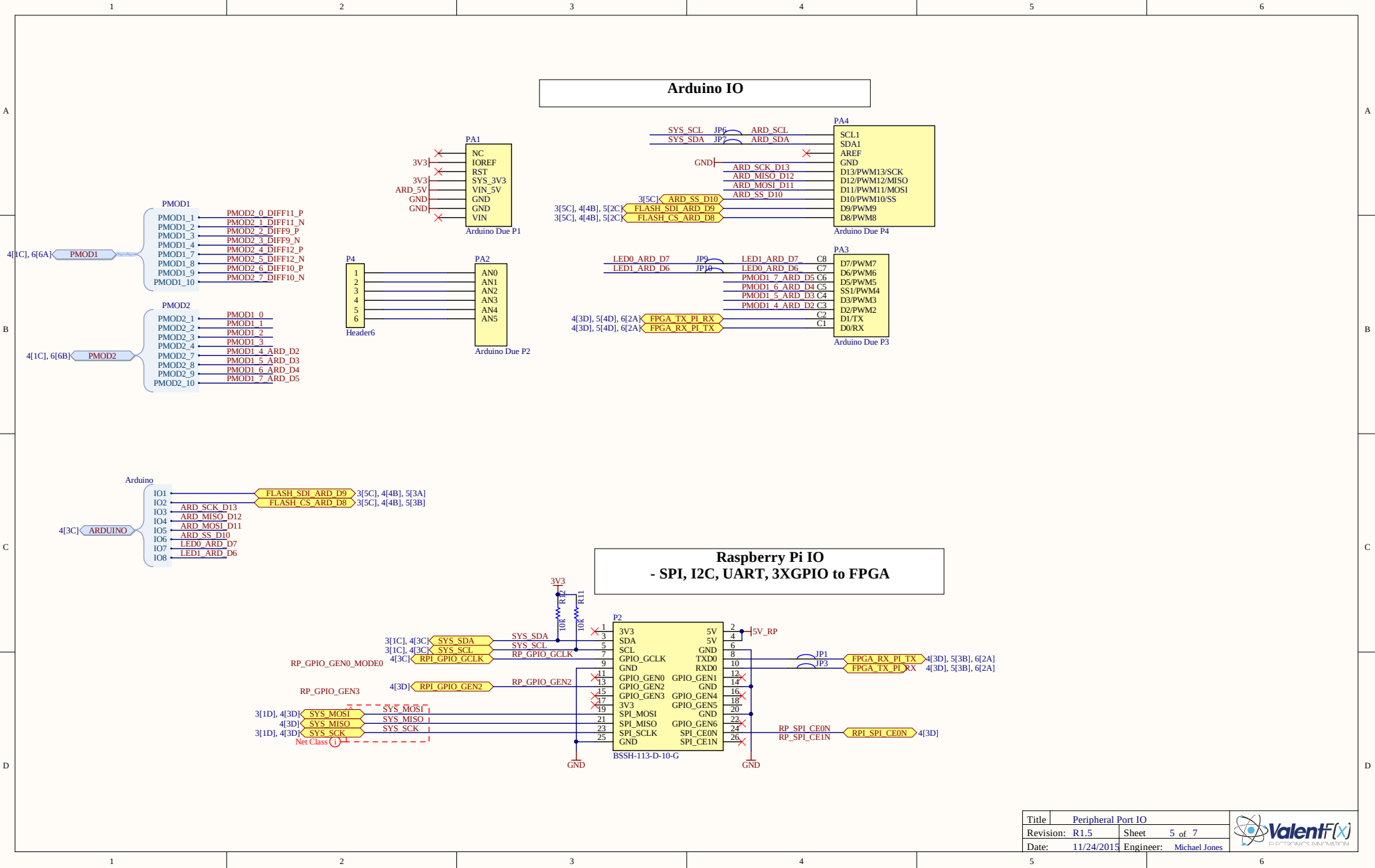
3

4

**Power:** By Default Power will be supplied by the .  
**Raspberry Pi**  
 Optionally power can be supplied through FPGA VIN header.

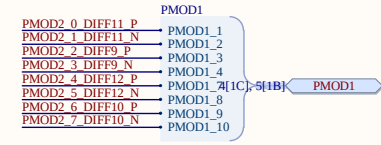
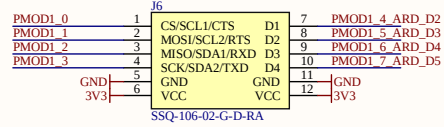




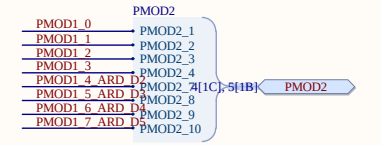
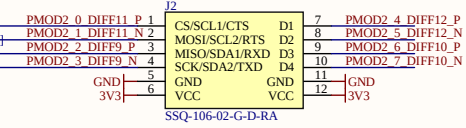


DIGITAL IO

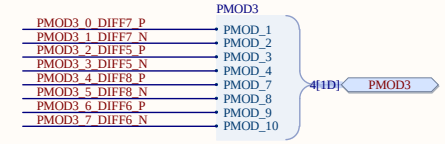
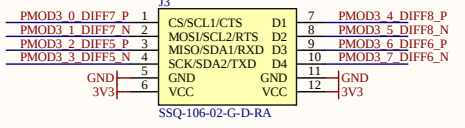
PMOD1



PMOD2



PMOD3



PMOD4

